Digital Systems Design with FPGAs and CPLDs

Ian Grout

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This textbook explains how to design and develop digital electronic systems using programmable logic devices (PLDs). Totally practical in nature, the book features numerous (quantify when known) case study designs using a variety of Field Programmable Gate Array (FPGA) and Complex Programmable Logic Devices (CPLD), for a range of applications from control and instrumentation to semiconductor automatic test equipment.

Key features include:

* Case studies that provide a walk through of the design process, highlighting the trade-offs involved.
* Discussion of real world issues such as choice of device, pin-out, power supply, power supply decoupling, signal integrity- for embedding FPGAs within a PCB based design.

With this book engineers will be able to:

* Use PLD technology to develop digital and mixed signal electronic systems
* Develop PLD based designs using both schematic capture and VHDL synthesis techniques
* Interface a PLD to digital and mixed-signal systems
* Undertake complete design exercises from design concept through to the build and test of PLD based electronic hardware

This book will be ideal for electronic and computer engineering students taking a practical or Lab based course on digital systems development using PLDs and for engineers in industry looking for concrete advice on developing a digital system using a FPGA or CPLD as its core.

*Case studies that provide a walk through of the design process, highlighting the trade-offs involved.
*Discussion of real world issues such as choice of device, pin-out, power supply, power supply decoupling, signal integrity- for embedding FPGAs within a PCB based design.

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